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KG Blemel - US Patent App. 09/977,140, 2001 - Google Patents
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to ... FPGA and non-volatile memory, digitizers, and other elec- tronic **hardware** described in ... controller
8. Program addresses are accomplished by an address **partitioning** and control ...

The Specification Language SpecC within the PARADISE Design Environment

psu.edu [PDF]

A Rettberg, FJ Rammig, A Gerstlauer, DD ... - ... and Design of ..., 2001 - books.google.com
... [Ha95] W. Hardt," An Automated Approach to HW/SW-Codesign". In IEEE Colloquium: **Partitioning**
in **Hardware-Software** Codesings. London, Great Britain, February 1995. [HaReK199] W. Hardt,
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M O'Neill, M Price, RJ Richter, S Shah, G Entine, J King ... - 1990 - oai.dtic.mil
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CJ Price - 1999 - books.google.com
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4. MOMPES: On Practical **Service**-Based Computing in Distributed **...**

 by H Shokry
the task will complete in **finite** time) but it would not .... possible

usage of **services** through **use case** graphs to **....** **state machine** of the unlocking **service** illustrates how **...** **Architecture**, www.autosar. org, Visited on 18 February **...** **Automotive Software** Engineering. In Proceedings of **...**
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   the **state machine** becomes a discrete-time Markov chain (DTMC). **.....** in **software** and partially in **hardware** (e.g., **Hardware** in the Loop (HiL) systems **...** in the system design phase, e.g., in the **automotive** area within the tool **.....** The mathematical model used in MBST is a **finite state**, time homogeneous, **...**
   www.ctit.utwente.nl/library/proceedings/wp0908.pdf

6. Embedded.com - **Software** Design Books

   Despite this inherent simplicity, the **use case** approach is frequently **...** of the relevant concepts including traditional **finite state machines**, **...** Executable UML: A Foundation for Model-Driven **Architecture ...** This is the first book on embedded systems to offer a unified approach to **hardware** and **software ...**
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   Software Architecture of Distributed **Automobile** System. **...** He
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... and the Applique+ computer, respectively, In documenting the BFIST M7 information system
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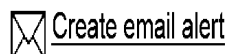**software** and **hardware**. Page 10. 4 Joel Rynn viewed as problematical. ...
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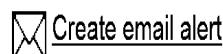Cited by 1294 - Related articles - Library Search - All 3 versions

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K Grimm - ... of the 25th international conference on Software ..., 2003 - portal.acm.org
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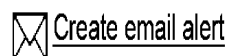
Computer-aided **architecture design** & optimized implementation of distributed automotive EE systems   psu.edu [PDF]
A Rajnak, A Kumar - 44th ACM/IEEE **Design** Automation ..., 2007 - ieeexplore.ieee.org
... We detail an already-deployed point tool in our flow, the **Network** Cluster Builder. ... to the AUTOSAR
standard to improve its applicability to system-level **design**. ... **User**-visible **vehicle** functionality is
implemented through interconnected application **Software** Components (SWCs). ...
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[PDF] **Effective vehicle teleoperation on the World Wide Web**
S Grange, T Fong, C Baur - IEEE International Conference on Robotics ..., 2000 - Citeseer
... 4 System **design** 4.1 Overview ... The **user** interface is connected to the rest of the system via
a persistent **network** link. Base Station. ... Robot. The teleoperated **vehicle** is equipped with
on-board sensors (including a pan-tilt camera) and a motion controller. ...
Cited by 38 - Related articles - View as HTML - BL Direct - All 15 versions

psu.edu [PDF]

User and operator perspectives in transit network design
A Ceder, Y Israeli - ... Journal of the Transportation Research Board, 1998 - Trans Res Board
... It is a histogram describing the number of passengers **on board** the transit **vehicle** versus the
transit ... to incorporate three operational planning ele- ments simultaneously at the macro and micro
levels: **network design**, frequency (timetable) setting, and **vehicle** scheduling ...
Cited by 26 - Related articles - BL Direct - All 4 versions

Deploying a wireless sensor network on an active volcano
G Werner-Allen, K Lorincz, M Welsh, O ... - IEEE Internet ..., 2006 - computer.org
... Before describing our **design** in detail, let's take a high-level view of our sensor node ... The MSP430
microcontroller provides **on-board** ADCs, but they're unsuitable for our application. ... A pair of
alkaline D cell batteries powered each sensor node — our **network's** remote location ...
Cited by 271 - Related articles - All 15 versions

psu.edu [PDF]

[PDF] **Design guidelines for safety of in-vehicle information systems**
A Stevens, A Quimby, A Board, T ... - Prepared for the ..., 2002 - transport-research.info
... Section 2 describes the different stages of the **design** process and considers briefly what each ...
Section 3 deals with the documentation and **user** instructions that may need to be provided ... 4
provides guidelines (if required) about how the IVIS should be fitted within the **vehicle**. ...
Cited by 19 - Related articles - All 7 versions

transport-research.info [PDF]

[PDF] **Adtranz: a mobile computing system for maintenance and collaboration**
D Siewiorek, A Smailagic, L Bass, J Siegel, R ... - Proceedings of the 2nd ..., 1998 - Citeseer
... The **on-board vehicle** system prototype gathers internal status data from embedded sensors in
systems such as propulsion and doors. ... Page 3. impact on the overall **design**. ... FarSite requires
a Winsock compliant Windows **network** interface, which is available with the WaveLAN. ...
Cited by 62 - Related articles - View as HTML - All 17 versions

psu.edu [PDF]

An autonomous guided vehicle for cargo handling applications
HF Durrant-Whyte - The International Journal of Robotics ..., 1996 - ijr.sagepub.com

... of the overall **vehicle** and **on-board** systems architecture; the development of a navigation system appropriate for large, outdoor, high-speed vehicles; and the experimental program leading to validation of the **vehicle** system. 1. Introduction This article describes the **design** of an ...
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[PDF] Robotic ocean vehicles for marine science applications: the european asimov project   uac.pt [PDF]

A Pascoal, P Oliveira, C Silvestre, L Sebastião ... - OCEANS- ..., 2000 - horta.uac.pt
... The **vehicle** is a major re-**design** of the MARIUS AUV, developed under the MAST-I and ... will be compiled, downloaded to, and run in real time on the computers installed **on-board** the ASC ... viewing all the modules inside each one of the vehicles as nodes in a local area **network**. ...
Cited by 62 - Related articles - View as HTML - BL Direct

On-board network for motor vehicles

A Moller, H Reimann, B Schurmann, G Wilts - US Patent 4,942,571, 1990 - Google Patents
... These harnesses are of very complex **design** and are prone to manufacturing errors since the fabrication of a harness is done predominantly manually. ... It has therefore been proposed already to replace the wire-harness of a motor **vehicle** by an **on- board network**, in which ...
Cited by 21 - Related articles

Design of automatic guided **vehicle** systems

WL Maxwell, JA Muckstadt - IIE Transactions, 1982 - informaworld.com
... accomplished using some type of guide path in or on the floor, **on-board** computers or ... a system and wants to determine how many vehicles are needed to support that **design**. ... Only the issue of **vehicle** requirements must be transportation **network** corresponding to this problem is ...
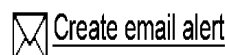Cited by 201 - Related articles - All 4 versions

A large-scale complex virtual environment for team training

TW Mastaglio, R Callahan - Computer, 1995 - ieeexplore.ieee.org
... Potential applications are shared **design** and other cooperative activities, long- distance telecommunications or ... a combat **vehicle** simulator, the crew has the same **vehicle** and **on-board** weapons controls and ... data packets transmitted over the FDDI LAN from all **network** entities. ...
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[BOOK] Finite element analysis of **electrical machines**
SJ Salon - 1995 - ieeexplore.ieee.org
... This book is an excel- lent introduction to the area of **finite** element analysis of the **electrical** ... trade
name may be used once, after which generic substitutes such as "method," "**software**," or "system ...
part, he or she should so **state**, provide the reference, and explain what part of the ...
Cited by 257 - Related articles - Library Search - All 4 versions

The Chinook **hardware/software** co-synthesis system   psu.edu [PDF]
PH Chou, RB Ortega, G Borriello - Proceedings of the 8th ..., 1995 - portal.acm.org
... To enable designers to rapidly evaluate different **architectural** templates and partitionings, Chinook
facilitates migration of functionality among processing el- ements and ... Constrained **software** gen-
eration for HW-SW systems. ... The Verilog **Hardware** Description Language. ...
Cited by 198 - Related articles - All 17 versions

Platform-based design and **software** design methodology for embedded systems   psu.edu [PDF]
A Sangiovanni-Vincentelli, G Martin - 2001 - ieeexplore.ieee.org
... architec- ture platform must be abstracted at a level where the application **software** can use a
high- level **interface** to the **hardware**: the API ... A **software** layer performs this abstraction, as shown
in Figure 1 (next page ... This layer wraps the essen- tial parts of the **architecture** platform: ...
Cited by 225 - Related articles - BL Direct - All 26 versions

[BOOK] Hardware-software co-design of embedded systems: the POLIS approach
F Balarin, P Di Giusto, A Jurecska, M Chiodo, C ... - 1997 - books.google.com
... 226 5.4 The **architecture** selection user **interface**. ... Lavagno then became the key **architect** of the
POLIS system. ... xviii **Hardware-Software** Co-design of Embedded Systems ■ Cadence, and in
particular, Patrick Scaglia, Vice-President of Research, Jim Rowson, Fellow, of the Aha ...
Cited by 768 - Related articles - BL Direct - All 3 versions

A model and methodology for **hardware-software** codesign
DE Thomas, JK Adams, H Schmit - IEEE Design and Test of ..., 1993 - computer.org
... In a **software** imple mentation, memory bandwidth is limited to that supported by the CPU and
memory subsystem. A **hardware** implementation can achieve high memory bandwidth at low
cost by employing a memory **architecture** tailored to the applica- tion. ...
Cited by 286 - Related articles - BL Direct - All 5 versions

[PDF] Hardware-software co-design of embedded systems   tkk.fi [PDF]
WH Wolf - Proceedings of the IEEE, 1994 - users.tkk.fi

... The Echelon Neuron **architecture** also uses a medium-performance serial link between the processors. **...** Embedded **software** can usually be thought of as a system of communicating processes, though the **...** a sequential **machine**, which we will use to refer to either a **hardware** or ...
Cited by 378 - Related articles - BL Direct - All 10 versions

Composing domain-specific design environments   escherinstitute.org [PDF]
Á Lédeczi, A Bakay, M Maroti, P Volgyesi, G ... - Computer, 2001 - ieeexplore.ieee.org
... GME's most important distinguishing characteristic is the consistent application of a metalevel **architecture** enforc- ing the **...** (computer **hardware** and **software ...** Various research groups have developed environ- ments exclusively targeting embedded **software** mod- eling formalisms **...**
Cited by 413 - Related articles - BL Direct - All 17 versions

Seeking solutions in configurable computing   caltech.edu [PDF]
WH Mangione-Smith, B Hutchings, D Andrews, A ... - Computer, 1997 - ieeexplore.ieee.org
... refining the emerging archi- tectures, producing more effective **software/hardware** APIs, better **...** for application development that incorporate the models of **hardware** reconfiguration, and ... Mirsky and A. DeHon, "MATRIX: A Reconfigurable Computing **Architecture** with Configurable **...**
Cited by 203 - Related articles - BL Direct - All 14 versions

[PDF] **A classification and comparison framework for software architecture description languages**   psu.edu [PDF]
N Medvidovic, RN Taylor - IEEE Transactions on **software** engineering, 2000 - Citeseer
... It is a mechanism for abstracting and reusing component **interface** elements. ... 5. As MetaH is used to specify both the **software** and the **hardware architecture** of an application, system is a hard- ware construct, while application pertains to both. Page 16. 16 **...**
Cited by 1715 - Related articles - View as HTML - BL Direct - All 45 versions

[PDF] **A formal approach to reactive system design: unmanned aerial vehicle flight management system design example**   psu.edu [PDF]
TJ Koo, B Sinopoli, A Sangiovanni-Vincentelli, S ... - Proc. of IEEE ..., 1999 - Citeseer
... Depending on the selected **architecture**, each task will take a speci ed number of clock to be executed (one clock ... depending on the selected target processor for **software**) and in di erent execution constraint (concurrency for **hardware**, mutual exclusion for **software**). ...
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Automotive: engine controllers, anti-lock brake controllers. ... The POLIS system is centered around a single **Finite State Machine**-like representation. ... flow graph for various micro-controller **architectures** (characterization data for ... **Interfaces** between different implementation domains (**hardware-software**) are ... embedded.eecs.berkeley.edu/Research/hsc/abstract.html - Cached

2. [PDF] Automatic **Hardware/Software Interface** Generation for Embedded System

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by C Son - 2006 - Cited by 1 - Related articles
of co-design **finite state machines** (CFSMs). SHIM [8] has the most similar features ... **Interface** Format (SHIFT) for **hardware/software interface** modules. ... **architecture** information, for example, the type of CPU and operating system. ...
jips.kips.or.kr/dlibrary/JIPS_v02_no3_paper1.pdf - Similar

3. Development of a PC-based Open **Architecture Software**-CNC System ...

by X MA - 2007 - Cited by 6 - Related articles
First, the **software** and **hardware** platform is chosen and **software** realization .... The **software interface** makes it easy to implement the SERCOS **interface**, ... For example, once in the **auto** capability **FSM**, a lower level **FSM** for the ...
linkinghub.elsevier.com/retrieve/pii/S1000936107600442 - Similar

4. **Hardware/Software** Communication and System Integration for ...

by S Vercauteren - 1997 - Cited by 18 - Related articles
Using this scheme, the channel adapter is in fact a **finite state machine** that .... **Interface**. Controller. Figure 15. **Architecture**

template with **hardware** I/O unit **....** **architecture** may still need to be implemented. In this case, we **auto-** **...**
www.springerlink.com/index/k8747x2lv3016566.pdf

5. **Software** development process - Wikipedia, the free encyclopedia

   ... teams to **interface** with customer/field teams testing the **software** to identify any real **...** Design and **architecture** emerge out of refactoring, and come after coding. **...** to solving **software** (and **hardware**) problems at the requirements, **...** **Finite state machine (FSM)** based methodologies allow executable **software** **...**
   en.wikipedia.org/wiki/**Software**_development_process
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6. An Open **Architecture** Motion Controller for CNC **Machine** Tools

   by Y Liu - 2008 - Related articles
   software and **hardware**. CNC system is demanded to have modular structure and ability of fast **....** library provides **interfaces** to define and modify **FSM**. **...**
   ieeexplore.ieee.org/iel5/4762424/4776129/04776146.pdf?... - Similar

7. **[PDF]** Generating Mixed **Hardware**/**Software** Systems from SDL Specifications

   File Format: PDF/Adobe Acrobat - Quick View
   by F Slomka - Cited by 23 - Related articles
   of modern **automotive** systems makes SDL also feasible for **.....** Figure 2: **Hardware architecture** of a EFSM. Finite-State. Machine. **Interface ....** Hardware. **Software**. Reg. Reg. ISR. ISR. **FSM** go fin. Figure 6: HW/SW-**Interface ...**
   citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.11.7814&rep...

8. Communication and **Interface** Synthesis on a Rapid Prototyping ...

   by YT Hwang - Related articles
   hardware software codesign. The target board contains a. TMS320C30 DSP processor and up **...** personal communication, **automotive** control and consumer electronics. Since various processing blocks **...** Numerous work [1-8] on codesign system **architectural ....** codes which contain both control **FSM** and **interface** circuitry. **...**
   doi.ieeecomputersociety.org/10.1109/ISSS.1998.730601

9. NSF Workshop on Logic Control for Manufacturing Systems

Finite State Automata (**State Machines**), Timed **State** Automata, and Petri Nets will be **...** PCs provide users with computational and human/**machine-interface ...** and generic PC/PLC-based **software/ hardware architecture** for the control of FMCs. **....** The **automotive** industry demands large quantities of parts to be produced **...**
www-personal.umich.edu/~tilbury/logiccontrol/abstracts.html
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10. A Practical Introduction to **Hardware/Software** Codesign

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Use case maps as architectural entities for complex systems   psu.edu [PDF]

RJA Buhr - IEEE Transactions on **Software** Engineering, 1998 - ieeexplore.ieee.org
... Index Terms—Use cases, scenarios, **software architecture**, system behavior, requirements,
design, **use case** maps. ——— F ——— ... The components may be **software** or **hardware**,
may be distributed in a network, and may be quite diverse, such ...
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[PDF] Requirements management for **automotive** systems development   psu.edu [PDF]

B Gebhard, M Rappl - SAE transactions, 2000 - Citeseer
... ABSTRACT **Electric's** and electronics' content in the high perfor- mance luxury segment's vehicles
is continuously increas- ing. ... N2 B2 I2 I3 Product **Architecture Software** C1 I1 I3 J1 B2 A1 **Hardware**
C2 I2 E1 N1 H1 Operations N2 B1 **Architecture** Design Page 7. 5 ...
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[PDF] Jini technology applied to railway systems   psu.edu [PDF]

T Nieva, A Fabri, A Benammour - 2nd International Symposium on ..., 2000 - Citeseer
... The purpose of Jini is to federate groups of **hardware** and/or **software** components into a single,
dynamic, distributed system. ... A **service** may be a computing program, a **hardware** device or a
component of the Jini system. ... Client Machine Figure 3. Jini-enabled system **architecture** ...
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[CITATION] Integration of Design Modeling Techniques: Operations Automation Systems Scenario

C ESCA - Proceedings, 1996 - IEEE Computer Society Press
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Integration of design modeling techniques: Operations AutomationSystems scenario   arizona.edu [PDF]

WW Owen, JW Rozenblit, C ESCA, ... - IEEE Symposium and ..., 1996 - ieeexplore.ieee.org
... power consumption history, their payment history, and in some cases their physical and **electrical**
location in ... its own user interface with geographic or schematic displays of the utility's **service**
temtory. ... **hardware architecture**, **software architecture**, and communications **architecture** ...
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Teaching more comprehensive model-based **software** engineering: experience with Objectory's **use case** approach   psu.edu [PDF]

R Coyne, B Bruegge, A Dutoit, D ... - **Software** Engineering ..., 1995 - Springer
... By decoupling these components in the system **architecture** modifications and iteration are more
easily ... FRIEND should be portable to existing **hardware** available in local governments. ... Engineering
phase of the project the client will negotiate with the **software** engineers an ...

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[PDF] Testing a software product line   psu.edu [PDF]

JD McGregor - 2001 - Citeseer

... Analysis **Architecture** Definition Design Component Development Product Integration ... of experience
from several projects that involve wire- less devices and provides the opportunity to illustrate
a number of issues including joint **hardware** and **software** development projects ...

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[CITATION] Proceedings: 34th International Conference on Technology of object-oriented languages and systems: TOOLS 34:
July 30-August 4, 2000: Santa Barbara, ...

Q Li - 2000 - IEEE Computer Society

[BOOK] Doing hard time: developing real-time systems with UML, objects, frameworks, and patterns

BP Douglass - 1999 - books.google.com

... Lau, The An of Objects: Object-Oriented Design and **Architecture** LeffingwellAVidrig, Managing ...
Process for Specifying Component-Based **Software** Szyperski, Component **Software**, 2E: Beyond ...
Resource-Limited Target Environments 82 2.11 Low-Level **Hardware** Interfacing 83 ...

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[BOOK] **Software** engineering: principles and practice   psu.edu [PDF]

H Van Vliet, H Van Vliet, JC Van Vliet - 1993 - Citeseer

... 291 11.5 **Software Architecture** Assessment . . . . **Software** was mostly developed by **hardware**
vendors and given away for free. ... Sometimes, **software** can be used as a **vehicle** for new
technologies, such as typesetting, the production of chips, or manned space trips. ...

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Specification, planning, and synthesis in a VHDL **design** environment

VI Nagasamy, N Berry, C Dangelo - IEEE **Design** and Test of ..., 1992 - computer.org
... The VHDL simulator helps the user verify the **design** at different stages. Dur- ing initial **specification**,
it verifies wheth- er the input VHDL description meets the IEEE **DESIGN** & TEST OF COMPUTERS ...
62 IEEE **DESIGN** & TEST OF COMPUTERS Page 6. the **architecture**: Goals ...
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software components

M Gunzert, A Nägele - pdse, 1999 - computer.org
... If there is not enough **electrical** power available for additional functions (ABS) the signal NOABS
is emitted. ... Table 2: mBrake – input and output signals 6.1 **Specification** of the Observers ... behave
fail-silent. o(¬FSUREADY ⇒ FAILSILENTMODE) 6.2 Internal **Design** of mBrake ...
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Using rapid prototyping in computer **architecture design** laboratories

JO Hamblen, H Owen, S Yalamanchili, B ... - ... Computer **architecture** ..., 1996 - portal.acm.org
... four hour, one quarter, courses taken in sequence by computer and **electrical** engineering students ...
and benchmark programs are being developed, students **design** the data path **architecture** of
a ... A written report or **design specification** describing the instruction set and data path ...
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Associative processing and processors

A Krikelis, CC Weems - Computer, 1994 - ieeexplore.ieee.org
... Lewin, DW, "Introduction to Associative Processors," Com- puter **Architecture**, AK Boulaye and
DW Lewin, eds., D. Reidel, Amsterdam, pp. 217-234, 1977. ... significant **design** issue in large
systems, especially for real-time applications. ... American Institute of **Electrical** Engineers. ...
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[BOOK] Digital systems **design** and prototyping: using field programmable logic and **hardware** description languages

Z Salcic, A Smailagic - 2000 - books.google.com
... 557 15 A VERILOG EXAMPLE: PIPELINED SIMP 559 15.1 SimP Pipelined **Architecture** 559 15.1 ...
Besides further emphasis on AHDL, as the main language for **design specification**, a further ... these
areas to the students studying digital system **design**, computer **design**, and related ...
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Introduction to high-level synthesis

DD Gajski, L Ramachandran - IEEE **Design** & Test of ..., 1994 - ieeexplore.ieee.org

... a rough block diagram of the chip **architecture**, which serves as a pre- liminaly, incomplete
specification. In some cases, the **architects** fur- ther refine this initial block dia- gram before **...** into
control steps, given the set of resources Scheduling: the **partitioning** of **design** behavior into **...**

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The **design** of RPM: an FPGA-based multiprocessor emulator

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K Öner, LA Barroso, S Iman, J Jeong, K ... - Proceedings of the ..., 1995 - portal.acm.org

... Department of **Electrical** Engineering - Systems University of Southern California Los Angeles,
CA 90089 **...** Whenever a condition is found to violate the protocol **specifications** in a controller **...**
workloads to determine the most promising designs, under the shortest possible **design ...**

Cited by 19 - Related articles - All 11 versions

An investigation into the use of **hardware**-in-the-loop simulation testing for automotive electronic control systems

IR Kendall, RP Jones - Control Engineering Practice, 1999 - Elsevier

... Once testing begins, every time an issue is found, it must be communicated to the supplier, **design**
changes made to **...** Fig. 1. The XK8 body control system **architecture**. **...** In addition to the **electrical**
signals, the real-time program must also have the ability to send and receive SCP **...**

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Recent developments in high-level synthesis

psu.edu [PDF]

YL Lin - ACM Transactions on **Design** Automation of Electronic ..., 1997 - portal.acm.org

... 6.5 Controller Issues Traditionally, the **control unit specification** is generated after the datapath
is synthesis completed. **...** The System **Architect's** Workbench from CMU [Thomas et al. **...** Recently,
HLS has been used in an embedded system **design** environ- ment. **...**
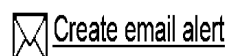
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System prototyping in the COBRA project

psu.edu [PDF]

G Koch, U Weinmann, U Kebschull, W ... - Microprocessors and ..., 1996 - Elsevier

... The **design** flow addressed by COBRA starts with a system level **specification** of a **...** J. Hughes,
FPGA based AS1C **Hardware** Emulator Architectures, School of **Electrical** and Computer **...** 4th
Eurochip Workshop on VLSI **Design** Training, Toledo, Spain, September 29October 1, 1993 **...**

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   to the **user interface** and it was subjected to public testing. ...... Division of MPU **electrical** connections into major sub
   systems. .... Reverse engineering this control system involves replacing both **hardware** and **software**. ...... As a last alternative,
   a **design specification** may be developed by the Government ...
   etd.ohiolink.edu/send-pdf.cgi/Bork%20John%20R.pdf?bgsu1120167127

2. [DOC] Central Pivot Irrigation System (CPIS) **Design Specification**

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   2.0 **Design** Overview 5. 2.1 **Architectural Design** 5. 2.2 **User Interface Design** 6 ... GUI – Graphical **User Interface** – A
   software interface that uses images and ... Valve **Control Unit** – An RCM3000 installed at a tower point in the CPIS. ...
   "Department of **Electrical** & Computer Engineering," "University of Idaho, ...
   www2.cs.uidaho.edu/~cs481_40/Documents/**Design**v1.doc

3. [PDF] **Hardware-software** codesign has been a

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   Evaluated **design**. Interface. System **specification**. **Hardware**. **Software**. Verified model. Model ... The models are then
   mapped to a **software-hardware architecture**. The ..... AICC **control unit's** graphical **user interface** during a safety test
   run. .... Jerzy W. Rozenblit is a professor of **electrical** and com- ...
   citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.32.425&rep...

4. [DOC] Job Title: Power Electronics Systems Engineer

   File Format: Microsoft Word - View as HTML
   ... a 100kW inverter (Motor **Control Unit** or MCU) for a battery **electric** vehicle program. .... o Draft system **design**
   specifications for **electric** drive and energy storage systems. .... Job Title: Senior **Hardware** Electronics Design Engineer .... o
   Customer **user interfaces** developed in Lab Windows or related **software** ...
   www.ieee-sem.org/Positions/Position%20Descriptions2.doc - Similar

5. Multilevel Testing for **Design** Verification of Embedded Systems

   by S Schulz - Cited by 13 - Related articles
   Conventionally, **hardware** and **software** development groups **design** and test these systems ... and then interactively map the
   specifications onto a mixed **hardware-software architecture**. ..... Our example focuses on the **design** of this device's **control**
   unit, ... This model also provided a **user interface** for the driver. ...
   doi.ieeecomputersociety.org/10.1109/54.990443 - Similar

6. [DOC] Teaching Automotive Embedded Controller System **Architecture** Basics ...

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Once the **system** requirements is designed and capture on the paper model, **...** In developing an **electrical** system **architecture**, the systems engineer will use the **...** Traditionally, the requirements are packaged as a **design specification** related to **.... User Interface** – Parts of a system that interacts with user. **...**
www.family-science.net/MaDon/.../TeachingEmbedded.doc - Similar

7. Using LabVIEW for Rapid Control Prototyping and **Hardware**-in-the **...**

Because of the open **architecture** of NI **software** and **hardware**, **...** Click here for a multimedia demonstration of creating a **user interface** for a Simulation model **...** implemented on target **hardware** that can be an electronic **control unit** (ECU), **...** Using the NI platform you can use your choice of **design software** with **...**
zone.ni.com/devzone/cda/tut/p/id/4566 - Cached - Similar

8. Course descriptions: **Electrical** and Computer Engineering: Norwich **...**

A **hardware design** language will be used and students will acquire **design** experience **...** Additional topics include peripheral **interface** control, interrupts, **...** Consideration of alternative machine **architectures**. **Software** influences on **.... specification**, preliminary **design**, **design** review and approval stages of the **...**
www.norwich.edu/academics/.../**electrical**Computer/courses.html - Cached - Similar

9. An **architecture** for **designing** reusable embedded systems **software ...**

If the **software architecture** takes on the defined structure that is illustrated **...** it will take on the form of a **hardware-software interface specification** (HSIS). **...** By explicitly **designing** an **interface** layer that is **hardware** dependent, **...** He has an MSE in computer and **electrical** control systems engineering from **...**
www.eetimes.com/**design**/...**design**/.../An-**architecture**-for-**design**ing-reusable-embedded-systems-**software**-Part-1 - Cached

10. **[PDF]** Model-Based **Design** Study and Evaluation of New HMI Concepts for **...**

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system **specification**, and demonstration of these new **...** with the proper memory size for the required **software** and **...** influence on the **user's** overall rating of the system. How to **design** the menu **....** system **architecture** and behavior, neither of which can be **...** isolating **hardware interfaces** is used throughout the **...**
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specification 4 , and a target **architecture** were available for this device, that was originally designed ..... New methodologies
are required in the **design** of digital **elec**- ... **Partitioning** between **hardware** and **software** is therefore a ...
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2. Computer - Wikipedia, the free encyclopedia

A computer does not need to be **electric**, nor even have a processor, nor RAM, ... In 1837, Charles Babbage was the first to
conceptualize and **design** a fully ..... The **control unit** (often called a control system or central controller) ..... When **software** is
stored in **hardware** that cannot easily be modified (such as ...
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Hardware in the Loop (HiL) Testing. Controller topology **specification** ... FEV offers full **design** support in hybrid powertrain
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FEV-Hybrid-VINS **software**, vehicle operational modes can be ...
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4. [PDF] Reverse Engineering a Microcomputer-based **Control Unit**

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compatible computer **hardware** running custom **programs** on a Fedora Core 2 GNU/Linux ..... AS2518 **electrical** schematic. ......
As a last alternative, a **design specification** may be developed by the Government through ...
etd.ohiolink.edu/send-pdf.cgi/Bork%20John%20R.pdf?bgsu1120167127

5. FPGA and Mixed FPGA-DSP Implementations of **Electrical** Drive Algorithms

by F Calmon - 2002 - Cited by 1 - Related articles
hardware/software solutions. The authors focus on the **design** flow and ... **software partitioning** and emulation. One major
problem is the system **specification** ... the **control unit**. The register bench provides 16-bits registers for dialog ... **Specification**
and **design** of embedded systems. Prentice-Hall, 1994. ...
www.springerlink.com/index/pggyuervbxl63g8d.pdf

6. FireOne: **Hardware** Details

This System consists of **hardware** and firing **software**. ..... The FireOne™ Firing Module is the most advanced digital **control
unit** available in any firing system. ... With the FireOne™ System the ability to fire **electric** matches is contained in the ... the
display **designer** must take the **wiring** power losses into ...
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7. [PDF] Incremental **hardware** estimation during **hardware/software** ...

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design model for a changed **partition** rather than re-estimating entirely. ... simulatable **specification** of system functionality, and

then explore .... size, control-logic size, and **wiring** size. Each is a function of one or ..... rithm for the constrained **hardware/ software partitioning** problem," in ...
www.cs.ucr.edu/~vahid/pubs/tvlsi95_hwest.pdf

8. Incremental **hardware** estimation during **hardware/software** ...

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size, control-logic size, and **wiring** size. Each is a function of one or ..... UNITY language in **hardware** and **software**," in Proc. Europ. **Design**. Automat. ... F. Vahid and D. Gajski, "**Specification partitioning** for system **design**," in Proc. ... Abstract-We present the analytical model and the **electrical** character- ...
ieeexplore.ieee.org/iel4/92/9148/00407006.pdf?arnumber=407006 - Similar

9. [PDF] AMC 20-1

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Omissions or errors in the system/**software specification**. ... 4.4.1(c) Precautions relating to **electrical** power supply and data from the aircraft ... An overheat condition results when the temperature of the electronic **control unit** is greater than ... 4.5.1(e) System **design Software** and Programmable Logic Devices ...
www.easa.europa.eu/.../Change%20Information%20to%20Decision%202007-019-R.pdf

10. Autosar transitioning from promise to products

Sep 1, 2007 ... This spring's release of the AUTOSAR 2.1 **specification** has ... Another supplier with AUTOSAR-capable **hardware** and **software** is NEC, ... Others are considering subsystems and will **partition** part of the **architecture** to continue to leverage ... driving cost-optimized **electrical** and **software design**, ...
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CPU design. Datapath. **Control unit**. Instruction cycle. Finite **state** machine and .... An **architecture**-centric approach to **software** development with emphasis on ... Operating system for **network** and distributed processing systems: naming, ... Use of **hardware** description languages for **design specification**. ...
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8. NFPA :: News & Publications :: NFPA Journal® :: Archived Issues ...

(2) Data received from the **network** shall not affect the operation of the fire ... The keystone for overall success is at the **design** and **specification** stage. ... the installation **wiring** integrity between the fire alarm **control unit** and ... the fire alarm system and controlled

electrical and mechanical systems must ...
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www.erts2010.org/Site/0ANDGY78/Fichier/.../ERTS2010_0018_final.pdf

3. **Hardware architecture** - Wikipedia, the free encyclopedia

A modern **automobile** runs vastly more **software** than the Apollo spacecraft. ... AKA human computer **interface**, or HCI; formerly called the man-**machine interface**. ... be used to alter the sequential **states** of a suitable (**hardware**) **machine**. However, a **hardware machine** and its **software** program can be designed to perform ... en.wikipedia.org/wiki/**Hardware_architecture** - Cached - Similar

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sary **hardware/software interfacing** parts required to integrate **software .....** Using this scheme, the channel adapter is in fact a **finite state machine** that **.....** **architecture** may still need to be implemented. In this case, we **auto- ...**
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software and **hardware**. CNC system is demanded to have modular structure and ability of fast **....** library provides **interfaces** to define and modify **FSM. ...**
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shall be ... In **software**/hardware partitioning, a typical methodology
relies on the description of appli- cation functions, of candidate
architectures, of the **mapping** of ... **hardware interface**. It is
compliant to the Secure Hardware Extension ...
www.eurecom.fr/util/publidownload.fr.htm?id=3132

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(This latter, special **interface**, is known as the computer human
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7. Steering your design through the AUTOSAR transition : John Day's **...**

   Sep 9, 2009 **...** Figure 1: The AUTOSAR **ECU** incorporates standardized elements developed with **...** The AUTOSAR **software** topology encourages a top-down design approach. **....** AUTOSAR is a promising new automotive **network architecture** that brings with **...** passing between otherwise incompatible **hardware interface** layers. **...**
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functions can be connected and **mapped** onto an **architecture** of **ECUs**. ... The distributed **network** design solutions from ... second example uses the actual built **ECU hardware**, termed "**hardware**-in-the-loop." In ... Figure 10: Source control **interface**. Conclusion. Major **software** evolutions occur when the full ... citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.118.1514...

9. **[PDF]** AUTOSAR Layered **Software Architecture**

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   The Layered **Software Architecture maps** the identified modules of the Basic **.....** Upper **Interface**: µC, **ECU hardware** and **network** type independent (goal) **...**
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10. AUTOSAR: New paths to **ECU software** - part 2

    Jul 18, 2010 **...** Just like the services, the **ECU's** I/O **interfaces** are also represented **...** Thanks to the systematic **hardware** abstraction offered by AUTOSAR the TIER1 **...** the service **mapping** for all diagnostic services of the new OEM. **...** enables early validation of the **ECU software architecture**. **...** EE Times **Network ...**
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J Ding, K Chakrabarty, RB Fair - IEEE Transactions on ..., 2001 - ieeexplore.ieee.org
... routing techniques with explicit area/delay tradeoff and simulta- neous **wire** sizing," in ... Index
Terms—Architectural optimization, integer linear programming, microelectrofluidics, **partition**
map, reconfigurable architecture, sched- uling. ... (A separate **electronic control unit** drives the ...
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[PDF] Development support for the design of distributed control systems in a road vehicle

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... Keywords: code-generation, **electronic control unit**, software development tools *
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Unit (ECU) Input ... automobile and the travel direction are displayed on a **map** stored in ...
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S Tribioli, V Giardinelli, F Rocco, F Cerrano - US Patent 5,506,065, 1996 - Google Patents
... isolate switch chamber 189 from reservoir 8. Solenoid valve 183 is driven by an **electronic control**
unit 199 which ... duration of the active phase and the period, on the basis of a memorized **map**
M which ... 2) comprises a **wire** 78 connected transversely so as to slide in a rod 79 fitted ...
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A Kojima, M Inagaki, S Miyoshi, F Arikawa, T ... - US Patent ..., 1995 - Google Patents
Page 1. United States Patent Kojima et al. US005458673A [11] Patent Number:
[45] Date of Patent: 5,458,673 Oct. 17,1995 [54] EXHAUST GAS PARTICULATE
PURIFYING PROCESS FOR INTERNAL COMBUSTION ENGINE ...
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VE Beneš - 1965 - books.google.com
... Chapter 7 A "Thermodynamic" Theory of Traffic 1. Introduction 223 2. Preliminaries 224 3.
Summary of Chapter 7 and Discussion 225 4. Equilibrium 230 5. The Maximum Entropy Principle
232 6. The Canonical Distribution 234 7. Properties of the **Partition** Function 237 8. A ...
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High performance hybrid micro-computer

KG Blemel - US Patent App. 09/977,140, 2001 - Google Patents
... These improvements do not require alternative **wiring** harness changes and do not affect ... micro-
controller 8. Program addresses are accomplished by an address **partitioning** and control ... with
slave serial and selectMAP functions for multiple FPGA program **mapping** and control. ...

Heating apparatus for vehicle

T Morikawa, H Ito, S Aoki, G Uchida, Y Kato, N ... - US Patent ..., 1999 - Google Patents
... 5. FIG. 9 is a flowchart of a segment of a program related to an engine ECU (**electronic control
unit**) in FIG. 5. FIG. ... 1, 3, and 4. FIG. 11 is a flowchart of a segment of a program related to an engine
ECU (**electronic control unit**) in a second embodiment of this invention. FIG. ...
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Pressure sensor using a pressure responsive magnetic film to vary inductance of a coil
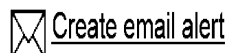
M Suzuki, F Murakami, Y Nakano, H ... - US Patent ..., 1994 - Google Patents
... 13 is a characteristic **map** showing an observed a signal component which has been ... 101 are
respectively connected deposition (CVD) and physical vapor deposition to an **electronic control
unit** (hereinafter referred ... like, other than by bonding as in "ECU") 35 via a lead **wire** or a ...
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Scholar | Articles and patents | | | - | 2001 | | include citations | ⊠ Create email alert Results **1 - 10** of about **352.** (**0.09** sec)

[PDF] Brake-by-wire without mechanical backup by using a TTP-communication **network**   tuwien.ac.at [PDF]

B Hedenetz, R Belschner - SAE transactions, 1999 - vmars.tuwien.ac.at
... A TTP controller **hardware** (see Figure 3) is supplied with an autonomous device called a **...** the
second chapter our main focus is to develop an **architecture** that guarantees **...** purpose we used
redundancy in sensors, power supply, electric components, **software**, and communication **...**
Cited by 37 - Related articles - View as HTML - BL Direct - All 3 versions

AIL: description of a global electronic **architecture** at the vehicle scale
A Panday, D Couderc, S Marichalar - ... of the conference on **Design**, ..., 2001 - portal.acm.org
... This component provides a generic **software** layer suitable for all **hardware** architectures. **...** Once
the **architecture** for a given vehicle is established, fully described and physically placed, the
compilation chain for each **ECU** freezes application **software** and middleware **...**
Cited by 5 - Related articles - All 2 versions

[PDF] Holistic Object-Oriented Modelling of Distributed Automotive Real-Time Control Applications   psu.edu [PDF]

J Axelsson - Proceedings of the 2nd IEEE International Symposium ..., 1999 - Citeseer
... The big challenge is to combine the use of higher level implementations with small **hardware**
resources. **...** In the uppermost, functional, layer, the underlying **architecture** is not considered at
all. Then, in the **software** layer, the same function has been partitioned, ie parts of it have **...**
Cited by 16 - Related articles - View as HTML - All 10 versions

[PDF] Titus–A Graphical **Design** Methodology for Embedded Automotive **Software**   unibw.de [PDF]

U Freund, A Burst, EGH Stuttgart - Workshop on Object-oriented ..., 2000 - cs.unibw.de
... Being an **architecture** description language [5][8], the TITUS **design** methodology comprises
design ... Since in automotive **software** resources are always allocated statically and dynamic
instantiation is **...** Client/server classes are independent from **hardware** and can be assigned to **...**
Cited by 2 - Related articles - View as HTML

[PDF] Development support for the **design** of distributed control systems in a road vehicle   210.101.116.115 [PDF]

R Moser, J Wiedemann, U Honekamp - 2000 - 210.101.116.115
... prevents a target-specific implementation of the code from a new mapping of this behavior on
a **hardware architecture**. **...** The link between an **ECU-software** developing tool and a **...** for further work
in the domain of distributed control and leads to a more sophisticated system **design. ...**
Related articles - View as HTML - All 3 versions

[CITATION] Performance Evaluation of a Multiple Networked in-Vehicle Embedded **Architecture**

L UMR7503 - 2000 IEEE International ..., 2000 - Institute of Electrical & Electronics ...
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DESIGN AND VERIFICATION OF AUTOMOTIVE NETWORKED SYSTEMS USING ARCHITECTURAL SIMULATION

M BMW AG - IFAC proceedings series, 1984 - books.google.com
... When setting up a model of **software** or **hardware**, there will ... cover pure technical risks in terms
of complexity handling, model transformation, simulation run time, ease of use, etc., and require
changes in the development process to get the benefits of **architecture** simulation in ...
Related articles

Multi-Protocol LAN Design & Implementation: A Case Study

S Hazari - THE Journal (Technological Horizons In Education), 1995 - questia.com
... Changes to **network** topology, **hardware** and **software** must be documented and
maintained on a continual basis. ... Client/Server: **Architecture** where computing
responsibility is distributed between a front-end and back-end program. ...
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Specification and design of electronic control units

psu.edu [PDF]

J Bortolazzi, T Hirth, T Raith - ... of the conference on European **design** ..., 1996 - portal.acm.org
... Figure 4. Current **network architecture** for distributed vehicle control functions ... PC WindowsNT
MatrixX STATEMATE AutoCode GNU C Compiler/Linker **Hardware** Connection Editor ... heavily
demand the following improvements: • Library based reuse of functionality and **software**. ...
Cited by 6 - Related articles - All 6 versions

Architectural trends in automotive electronics

U Kiencke, T Kytölä, KJ Neumann - Annual Reviews in Control, 1996 - Elsevier
... When the result of the analysis is finally positive, a more detailed **design** of the **hardware** and
the **software** are performed separately ... Systems Hardwaresoftware codesign may be applied either
by a semiconductor supplier or a customer to determine the ;LC **architecture** for a ...
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[PDF] Ptolemy: A framework for simulating and prototyping heterogeneous systems

psu.edu [PDF]

J Buck, S Ha, EA Lee, DG Messerschmitt - International Journal of ..., 1994 - Citeseer
... pro- cessing **software**. An interesting distinction between MQ and many other domains
is that graphical ... the Thor **hardware** timing simulator [32]). We are also beginning to
design a domain with finite-**state**- machine semantics. The ...
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Java-based graphical user interface for MRUI, **a software** package for quantitation of in vivo/medical magnetic resonance spectroscopy signals

A Naressi, C Couturier, I Castang, R De Beer, D ... - Computers in biology ..., 2001 - Elsevier
... Fig. 1. Directory/package **hierarchy** of the MRUI **software** system. ... Fig. 5. UML class **diagram** of
mrui.quantitation.Amares.**GUI**.AmaresSeriesWin. View Within Article. ... In the Java-based
MRUI-**GUI software**, described in this paper, the nomenclature preprocessing is used, however. ...
Cited by 104 - Related articles - BL Direct - All 6 versions

[BOOK] Refactoring: improving the **design** of existing code

psu.edu [PDF]

M Fowler, K Beck - 1999 - books.google.com
... from one class to another, pull some code out of a method to make into its own method, and push
some code up or down a **hierarchy**. Yet the cumulative effect of these small changes can radically
improve the **design**. It is the exact reverse of the normal notion of **software** decay. ...
Cited by 3436 - Related articles - Library Search - BL Direct - All 35 versions

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L Maciaszek - 2007 - Pearson Education
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Model-integrated development of embedded **software**

psu.edu [PDF]

G Karsai, J Sztipanovits, A Ledeczi, T ... - Proceedings of the ..., 2003 - ieeexplore.ieee.org
... Circuits **Hardware** Description Language from the **hardware** models specifying the **hardware**
architecture, and ... system, with a general-purpose editing engine, separate viewer-controller **GUI**,
and a ... the composed designs are crucial in model-based system/**software** development ...
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UML-based multiprocessor SoC **design** framework

psu.edu [PDF]

T Kangas, P Kukkala, H Orsila, E Salminen, ... - ACM Transactions on ..., 2006 - portal.acm.org
... A screenshot of the **GUI** is shown in Figure 3. With Koski **GUI**, it is ... The library also contains both

hardware and **software** that is utilized for profiling the **state** ... Similarly, the **design** automation library
contains **hardware** monitors for evaluating the **hardware**-related performance ...
Cited by 68 - Related articles - All 5 versions

[PDF] A CAD suite for high-performance FPGA **design**   psu.edu [PDF]

B Hutchings, P Bellows, J Hawkins, S Hemmert, ... - Proceedings of the ..., 1999 - Citeseer
... libraries), including: the actual circuit description, the run-time control, the **GUI** (if there ... Finally,
inheritance provides a natural way of manually partitioning classes into **software** and **hardware** ...
therefore, the core structural **design** class, Logic class, provides a quick-**hardware** API to ...
Cited by 125 - Related articles - View as HTML - All 12 versions

[PDF] Object-oriented **software design** with axiomatic **design**   axiomaticdesign.com [PDF]

SH Do, NP Suh - Proceedings of ICAD2000, Cambridge, MA, 2000 - axiomaticdesign.com
... that used for **hardware** systems and components, and thus the integration of **software** and
hardware design becomes a ... DP1: Element characteristics DP2: **GUI** with **window** ... Whenever the
software designer categorizes module groups as classes using the full **design** matrix, they ...
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Axiomatic **design** of **software** systems   axiomaticdesign.com [PDF]

NP Suh, SH Do - CIRP Annals-Manufacturing Technology, 2000 - Elsevier
... Acclaro is an interactive **software** system for designers of **hardware**, **software**, and systems. ... Support
ease of use while Graphical User using **software** Interface (**GUI**) Provide efficient ... The Acclaro
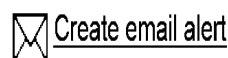**software design** has nine level **hierarchies** and well over 1000 leaves, which may ...
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Finite-**state** testing and analysis of graphical user interfaces   uni-paderborn.de [PDF]

F Belli - issre, 2001 - computer.org
... 1 (easy to understand, but in- formal presentation of the **GUI**) into Fig. ... Monitoring requires a
special structure of **software** which must be designed carefully, considering the methods and
principles of the modern **Software** Engineering (**Design** for Testability). ...
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[PDF] A new approach to functional and software structure for engine management systems-BOSCH ME7   funpic.de [PDF]

J Gerhardt, H Hönninger, H Bischof - SAE transactions, 1998 - cloudy.cl.funpic.de

... unit in the early 70's, injection and ignition were integrated into one single **electronic control unit**
during the ... The introduction of electronic throttle control (ETC) as a drive-by-**wire** system with it's ...
timing and cylinder individual fuel cut-off could be described in a five dimension **map**. ...

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[PDF] Development support for the design of distributed control systems in a road vehicle   210.101.116.115 [PDF]

R Moser, J Wiedemann, U Honekamp - 2000 - 210.101.116.115

... Keywords: code-generation, **electronic control unit**, **software** development tools ... which intend to
replace mechanical solutions by electronic ones, eg drive/brake by **wire**, enforce this ... the other
hand prevents a target-specific implementation of the code from a new **mapping** of this ...

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High performance hybrid micro-computer

KG Blemel - US Patent App. 09/977,140, 2001 - Google Patents

... These improvements do not require alternative **wiring** harness changes and do not affect ... micro-
controller 8. Program addresses are accomplished by an address **partitioning** and control ... with
slave serial and selectMAP functions for multiple FPGA program **mapping** and control. ...

[PDF] THE UNIVERSITY OF CALGARY   ucalgary.ca [PDF]

R Mehrotra - 1998 - dspace1.acs.ucalgary.ca

... TBI TDC TWC Air-fuel ratio (mass basis) Stoichiometric air-fuel ratio Bottom dead center
Drive-by **wire** **Electronic control unit** Exhaust gas ... tables to closed-loop computer based diagnostic
reflects the ongoing research effort and improvement m both **hardware** and **software**. ...

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The Specification Language SpecC within the PARADISE Design Environment   psu.edu [PDF]

A Rettberg, FJ Rammig, A Gerstlauer, DD ... - ... and Design of ..., 2001 - books.google.com

... Hence, CHaRy supports the **mapping** of controller models to a number of tasks (**partitioning**),
the extraction of their computation times (timing analysis), and the assignment of tasks to a
processor network ... In IEEE Colloquium: **Partitioning** in **Hardware-Software** Codesings. ...

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System for controlling the output power of a motor vehicle

M Itoh, K Yamada, H Yoshida, K Ohtake, Y ... - US Patent ..., 1993 - Google Patents

... The motor vehicle output power control system includes an **electronic control unit** for controlling an actuator to lower the output drive torque of the engine, independent ... Ada 9X Program Structure and Compilation Issues, Draft **Mapping** Document, Feb. ... "System **Partitioning** and Its ...

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Session 1A

LE Porter, W Merrill - ... on Industrial & Engineering Applications of ..., 1988 - books.google.com

... 3. The **mapping** of the state to hypotheses is neither unique nor one-to-one **mapping**. ... Test-Obiect A test-object describes a test to be performed such as taking a voltage reading at a test point, or visually inspecting the connection of a piece of **wire**. ...

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[PDF] Computer-aided **Architecture Design** & Optimized Implementation of ...

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by A Rajnak - Cited by 4 - Related articles

architecture design, **scheduling** of **networks** and **tasks** distributed across multiple ECUs, **harness design** .... **hardware interfaces** are specified (e.g. CPU type, communication ... configuring and building the **software** image for a particular **ECU**. ... content of variants, and the **distribution** of options. Factors ...

videos.dac.com/44th/papers/31_4.pdf

2. [PDF] Open **Architecture** Solution for **Hardware**-in-the-Loop Testing

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test of the J1939 **network**, actual **ECU hardware** must be ... cabling and **harnesses**, and fabrication complexity ... One of the early **design** decisions facing the team was ... **interface** options, however the **software** was less well .... in a synchronous manner by **scheduling** each of the messages on the bus. ...

www.mathworks.com/mason/tag/proxy.html?dataid=11240&fileid...

3. **Design software** - engineering **software** | process **software** ...

water, sewer and stormwater **network design software** Bentley Systems Europe B V .... + **ECU Software** + Process Management + Solutions for... + **Hardware** ... The convenient and easy-to-operate user **interface** of the **Network** Designer CAN ... to interactively **design** the FlexRay **schedule** forms the basis for the tool. ...

www.directindustry.com/.../**design-software**-77666.html

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4. Autosar standards open the door to auto **design** optimization, Part 1

Oct 1, 2009 ... The emerging Autosar automotive **design software**

standard began as the product **...** Autosar SWCs interact via an application programming **interface** (API) known as **...** and might even add cost to other components such as the wiring **harness**. **...** and it knows the throughput of the **ECU's network** controller. **...** www.eetimes.com/**design**/...**design**/.../Autosar-standards-open-the-door-to-auto-**design**-optimization-Part-1 - Cached

5. **[PDF]** AUTOSAR Basic **Software** for Complex Control Units

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Architecture) has been a de facto standard for **software ...** automotive **ECU networks** is to use domain controllers. A **...** **distribution** of control functions, they also offer new **...** In addition to **hardware** safety, the **ECU-software** has also **...** **Hardware** **interfaces** like EB's own EB 61x0 are then used **...** www.date-conference.com/proceedings/PAPERS/2010/.../03.8_3. PDF

6. COMPLEXITY MANAGEMENT FOR VEHICLE ELECTRICAL/ ELECTRONIC **...**

the **harness** (or wiring) domain responsible for **designing** the wire **harness ....** To illustrate, the **ECU design** data 219 may be formatted for use by a **....** The **network interface** device 320 can provide connectivity to a **network** 326, e.g., **.....** repartitioning the **distribution** of **tasks** and functions between **hardware ...** www.faqs.org/patents/app/20100031212 - Cached - Similar

7. ETAS - eNewsletter, Issue 1/2010 - United Kingdom & Nordic **...**

Jun 30, 2010 **...** New **ECU** and bus **interface** modules. The new compact **hardware** modules of the **...** A new approach for process-oriented and tool based automation of calibration **tasks ...** Thanks to the open **architecture** LABCAR customers are able to **...** CLONE RTPC in RTPC Web **Interface** for easy **distribution** of RTPC set up **...** www.etas.com/en/etas_uk_contact.php?regionalPage=/regional/... - Cached

8. **[PDF]** Microsoft PowerPoint - Workshop_ETFA09_Navet

File Format: PDF/Adobe Acrobat - Quick View
by N Navet - 2009 - Related articles
Sep 25, 2009 **...** wiring **harness** is tailored to the options. Picture

from [10] **...** Optimizing the use of **networks** is becoming an **...** Intra-
and inter-**ECU** Communication. MW hides the **distribution ...** But
task scheduling has to be adapted otherwise **....** [5] AUTOSAR – an
open standardized **software architecture** for the **...**
www.realtimeatwork.com/wp-content/.../
Workshop_ETFA09_Navet_bw.pdf

9. A Conceptual Data Model for the **Architecture** Exploration of **...**

by P Giusto - 2007 - Related articles
The **design** of in-vehicle **distributed** embedded **...** definition of
software task execution **architectures** and the **...** Analysis. Wiring
Harness. **Design**. **Software**-. **Hardware**. Verification and.
Detailed **....** has only a functional **interface**, an **ECU** owns both a **...**
periodic vs. data-driven) – the bus **schedule** may be not **...**
ieeexplore.ieee.org/iel5/4296570/4296571/04296683.pdf?arnumber...

10. Adam Elbirt - LinkedIn

Greater Boston Area - Digital Systems Group Leader and Senior
Member of Technical Staff at Draper Laboratory
Computer **architecture**, digital system **design**, real-time and
embedded systems, **... scheduling** and budgeting of all **hardware**
development **tasks** and assigned personnel, **...** Member of the
Network & Systems Security (NSS) Laboratory within the **.... Tasks**
included Host to **ECU software interface** modification and
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